ROCK 4C+ schematic

V1.411
CONTENT INDEXING

01. Cover Page
02. CONTENT
03. DP to HDMI 2K
04. HDMI 4K
05. RK3399 Power/GND
06. RK3399 PMU Controller
07. RK3399 DRAM Controller
08. RK3399 FLASH/MMC Controller
09. RK3399 USB/USIC Controller
10. RK3399 SAR-ADC/Key
11. RK3399 VideoInput
12. RK3399 VideoOutput
13. RK3399 GPIO
14. RK3399 PCIE
15. RAM-LPDDR4(200P)
16. EMMC
17. WIFI/BT MODULE
18. TF Card
19. CAM
20. Audio
21. CONNECTOR
22. Power-PMIC RK809-3
23. USB3.0 OTG Port
24. RTL8211F
25. Power TYEPC IN

ROCK 4C+ A4

Tuesday, November 29, 2022
Note: All the Power filter capacitors should be placed close to the power pins of RK3399.
Note:
GPIO0_B1/PMUIO2_VOLSEL defined as a set pin for PMUIO2 part voltage domain after power-on reset. It is pull-down for 1.8V or pull-up for 3.0V, and later changed to output mode by software.

**Pull-up for PMUIO2 domain work in 3.0V mode**

**Pull-down or floating for PMUIO2 domain work in 1.8V mode**
<table>
<thead>
<tr>
<th>BOM ID</th>
<th>Item</th>
<th>Rup</th>
<th>Rdown</th>
<th>VOL</th>
<th>VERSION</th>
</tr>
</thead>
<tbody>
<tr>
<td>LEVEL1</td>
<td>DNP</td>
<td>100K</td>
<td>0V</td>
<td></td>
<td>A</td>
</tr>
<tr>
<td>LEVEL2</td>
<td>100K</td>
<td>20K</td>
<td>0.3V</td>
<td></td>
<td>B</td>
</tr>
<tr>
<td>LEVEL3</td>
<td>100K</td>
<td>51K</td>
<td>0.6V</td>
<td></td>
<td>C</td>
</tr>
<tr>
<td>LEVEL4</td>
<td>100K</td>
<td>100K</td>
<td>0.9V</td>
<td></td>
<td>D</td>
</tr>
<tr>
<td>LEVEL5</td>
<td>100K</td>
<td>200K</td>
<td>1.2V</td>
<td></td>
<td>E</td>
</tr>
<tr>
<td>LEVEL6</td>
<td>100K</td>
<td>499K</td>
<td>1.5V</td>
<td></td>
<td>F</td>
</tr>
<tr>
<td>LEVEL7</td>
<td>100K</td>
<td>DNP</td>
<td>1.8V</td>
<td></td>
<td>G</td>
</tr>
</tbody>
</table>
Note: All the Power filter capacitors should be placed close to the power pins of RK3399.