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# Radxa Display 10 FHD

10-inch FHD Touchscreen Module

Revision 1.1

2023-08-22



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# 1 Revision Control Table

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| Version | Date       | Changes from previous version |
|---------|------------|-------------------------------|
| 1.0     | 01/06/2023 | First Version                 |
| 1.1     | 22/08/2023 | Update Information            |

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## 2 Overview



Figure 1: Radxa Display 10FHD

Radxa Display 10FHD's Display mode is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel and a driving circuit. This TFT LCD has a 10.1(16:10) inch diagonally measured active display area with (1200 horizontal by 1920 vertical pixel) resolution.

## 3 Specification

- Panel Size: 10.1 inch
- Compatible with NTSC & PAL system
- Image Reversion: UP/DOWN and LEFT/RIGHT
- ROHS design

- Outline Dimension: 155(H) x 246.16(V) x 5.95(D) mm
- Display area: 135.36(H) x 216.58 (V) mm
- Number of Pixel: 1200RGB (H) x 1920 (V) pixels
- Pixel pitch: 0.0564 (H) x 0.1692(V)s mm
- Pixel arrangement: RGB Vertical stripe
- Display mode: Normally Black
- Color Filter Array: RGB vertical stripes
- Backlight: White LED
- Electrical Interface: MIPI

## 4 Electrical Characteristic

### 4.1 Absolute Maximum Ratings

| Item                  | Symbol | Values |      | Unit |
|-----------------------|--------|--------|------|------|
|                       |        | Min.   | Max. |      |
| Power Voltage         | VDD    | 3.0    | 3.6  | V    |
| Operation Temperature | TOP    | -20    | 60   | °C   |
| Storage Temperature   | TST    | -25    | 70   | °C   |

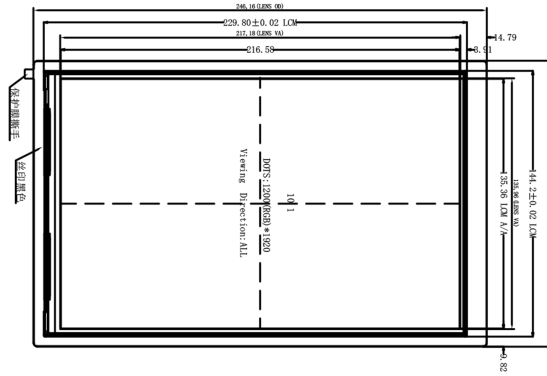
## 5 Pin Description

| PIN | Name     | Description   | PIN | Name     | Description                            |
|-----|----------|---|-----|----------|--|
| 1   | NC       | No connection                                       | 21  | MIPI_3P  | +MIPI differential data input          |
| 2   | VDD      | Power Voltage for digital circuit 3.3V              | 22  | GND      | Ground                                 |
| 3   | VCCIO    | Power Voltage for digital circuit 1.8V <sup>1</sup> | 23  | GND      | Ground                                 |
| 4   | INT1     | INT 1   | 24  | TP_RESET | External interrupt to the Host Reset   |
| 5   | Reset    | Global Reset Pin 1.8V <sup>2</sup>                  | 25  | TP_VCC   | Power Voltage for digital circuit 3.3V |
| 6   | INT2     | INT 2   | 26  | TP_INT   | External Low is active                 |
| 7   | GND      | Ground  | 27  | TP_SDA   | I2C data input and output              |
| 8   | MIPI_0N  | -MIPI differential data input                       | 28  | TP_SCL   | I2C clock input                        |
| 9   | MIPI_0P  | +MIPI differential data input                       | 29  | GND      | Ground                                 |
| 10  | GND      | Ground  | 30  | GND      | Ground                                 |
| 11  | MIPI_1N  | -MIPI differential data input                       | 31  | VDD      | Power Voltage for digital circuit 3.3V |
| 12  | MIPI_1P  | +MIPI differential data input                       | 32  | VDD      | Power Voltage for digital circuit 3.3V |
| 13  | GND      | Ground  | 33  | GND      | Ground                                 |
| 14  | MIPI_CKN | -MIPI differential clock input                      | 34  | GND      | Ground                                 |
| 15  | MIPI_CKP | +MIPI differential clock input                      | 35  | LED-     | Power for LED backlight (Cathode)      |
| 16  | GND      | Ground  | 36  | LED-     | Power for LED backlight (Cathode)      |
| 17  | MIPI_2N  | -MIPI differential data input                       | 37  | NC       | No connection                          |
| 18  | MIPI_2P  | +MIPI differential data input                       | 38  | NC       | No connection                          |
| 19  | GND      | Ground  | 39  | LED+     | Power for LED backlight (Anode)        |
| 20  | MIPI_3N  | -MIPI differential data input                       | 40  | LED+     | Power for LED backlight (Anode)        |

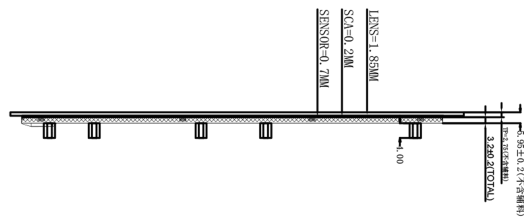
<sup>1</sup>The 3 PIN and 5 PIN should be the same as 1.8v or 3.3v

<sup>2</sup>The 3 PIN and 5 PIN should be the same as 1.8v or 3.3v

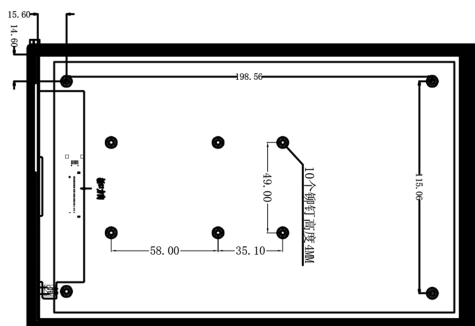
## 6 Mechanical Specification



Front



Side



Back

### 7 Availability

Radxa guarantees availability of the Radxa Display 10FHD until at least September 2033.

### 8 Support

For support please see the hardware documentation section of the [Radxa Wiki](#) website and post questions to the [Radxa forum](#).